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Dated: March 19, 2002 Signature:

(Kimberly V. Perry)

ERA 3.3-018 CONT CONT II CONT II CON
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

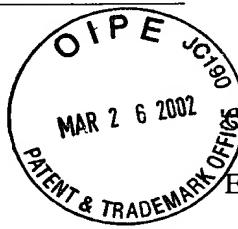
In re Patent Application of:
Khandros et al.

Application No.: 09/691,807

Filed: October 19, 2000

For: STACKED CHIP ASSEMBLY

Commissioner for Patents
Washington, DC 20231



Group Art Unit: 2815

Examiner: S. Clark

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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: March 19, 2002

Respectfully submitted,

By

Kimberly V. Perry

Registration No.: 43,612

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Commissioner for Patents
Washington, DC 20231

REQUEST FOR CONSIDERATION OF INFORMATION UNDER 37 CFR § 1.97(c)

Dear Sir:

It is respectfully requested that the references cited in the enclosed form be considered pursuant to 37 C.F.R. § 1.97(c). Please charge deposit account No. 12-1095 in the amount of \$180.00 pursuant to 37 C.F.R. § 1.17(p). In the event that any additional fee is due in connection with the present request, the same should be charged to our deposit account No. 12-1095.

Dated: March 19, 2002

Respectfully submitted,

By

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